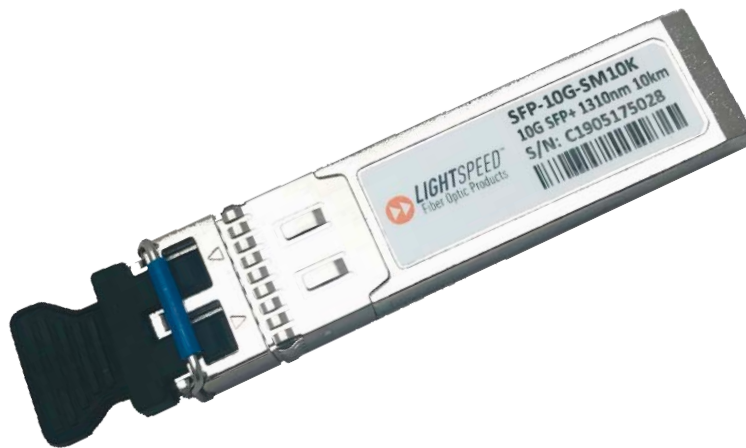


10GBASE-LR SFP+ 1310nm 10km DOM Transceiver

SFP-10G-SM10K



Application

- 10GBASE-LR/LW 10G Ethernet
- 1200-SM-LL-L 10G Fibre Channel

Features

- Hot-pluggable SFP+ footprint
- Supports 9.95 to 10.5Gb/s bit rates
- Power dissipation < 1W
- RoHS-6 compliant (lead-free)
- Industrial temperature range : -40° C to 85° C
- Single 3.3V power supply
- Maximum link length of 10km
- Uncooled 1310nm DFB laser
- Receiver limiting electrical interface
- Duplex LC connector
- Built-in digital diagnostic functions

Description

10Gb/s Enhanced Small Form Factor Pluggable SFP+ transceivers are designed for use in 10-Gigabit Ethernet links up to 10km over Single Mode fiber. They are compliant with SFF-8431, SFF-8432 and IEEE 802.3ae 10GBASE-LR/LW, and 10G Fibre Channel 1200-SM-LL-L. Digital diagnostics functions are available via a 2-wire serial interface. The transceiver is a “limiting module” , i.e., it employs a limiting receiver. Host board designers using an EDC PHY IC should follow the IC manufacturer’ s recommended settings for interoperating the host-board EDC PHY with a limiting receiver SFP+ module. The optical transceiver is compliant per the RoHS Directive 2011/65/EU.

Product Specifications

I. General Specifications

Data Rate Specifications	Symbol	Min	Typ.	Max	Units	Ref.
Bit Rate	BR	3.144		11.3168	Gb/s	1
Bit Error Ratio	BER			10 ⁻¹²		2
Max. Supported Link Length	L MAX			40	km	1

Notes:

1. 10GBASE-LR, 10GBASE-LW, 1200-SM-LL-L 10GFC.
2. Tested with a 2³¹ – 1 PRBS.

II. Absolute Maximum Ratings

Parameter	Symbol	Min	Typ.	Max	Unit	Ref.
Maximum Supply Voltage	V _{CC}	-0.5		4.0	V	
Storage Temperature	T _S	-40		85	° C	
Case Operating Temperature	T _{OP}	0		70	° C	
Relative Humidity	RH	0		85	%	1
Receiver Optical Damage Threshold	RxDamage	5			dBm	

Note:

Non-condensing.

III. Electrical Characteristics (TOP = 0 to 70 °C, VCC = 3.14 to 3.46 V)

Parameter	Symbol	Min	Typ.	Max	Unit	Ref.
Supply Voltage	V _{CC}	3.14	3.30	3.46	V	
Supply Current	I _{CC}		200	285	mA	
Transmitter						
Input differential impedance	R _{in}		100	120	Ω	1
Differential data input swing	V _{in,pp}	180		850	mVpp	
Transmit Disable Voltage	V _D	2	50	V _{CC}	V	
Transmit Enable Voltage	V _{EN}	V _{ee}		0.8	V	

Receiver

Differential data output swing	Vout,pp	300		850	mV	2,5
Output rise time and fall time	Tr, Tf	28			ps	3
LOS Fault	V _{LOS fault}	2		Vcc	V	4
LOS Normal	V _{LOS norm}	Vee		0.8	V	4
Power Supply Noise Tolerance	VccT/VccR		Per SFF-8431 Rev 3.0		mVpp	

Notes:

1. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
2. Into 100 differential termination.
3. 20 – 80%. Measured with Module Compliance Test Board and OMA test pattern. Use of four 1’ s and four 0’ s sequence in the PRBS 9 is an acceptable alternative.
4. LOS is an open collector output. Should be pulled up with 4.7k – 10k on the host board. Normal operation is logic 0; loss of signal is logic 1.
5. The transceiver is a “limiting module” , i.e., it employs a limiting receiver. Host board designers using an EDC PHY IC should follow the IC manufacturer’ s recommended settings for interoperating the host-board EDC PHY with a limiting receiver SFP+ module.

IV. Optical Characteristics (TOP = 0 to 70 °C, VCC = 3.14 to 3.46 V)

Parameter	Symbol	Min	Typ.	Max	Unit	Note
Transmitter						
Optical Modulation Amplitude (OMA)	P_{OMA}	-5.2			dBm	
Average Launch Power	P_{AVE}	-8.2		+0.5	dBm	1
Optical Wavelength	λ	1260		1355	nm	
Side-Mode Suppression Ratio	SMSR	30			dB	
Optical Extinction Ratio	ER	3.5			dB	
Transmitter and Dispersion Penalty	TDP			3.2	dB	
Average Launch power when Tx is OFF	P_{OFF}			-30	dBm	
Tx Jitter	Txj	Per 802.3ae requirements				
Relative Intensity Noise	RIN			-128	dB/Hz	
Receiver						
Receiver Sensitivity (OMA) @ 10.3Gb/S	R_{SENS1}			-12.6	dBm	2
Receiver Sensitivity (OMA) @ 10.3Gb/s	R_{SENS2}			-10.3	dBm	3
Average Receive Power	P_{AVE}	-14.2		+0.5	dBm	
Optical Center Wavelength	λ_C	1260		1600	nm	
Receiver Reflectance	Rrx			-12	dB	

LOS De-Assert	LOS De-Assert	LOS_D		-17		dBm
LOS Assert		LOS_A	-30			dBm
LOS Hysteresis			0.5			dB

Notes:

1. Average power figures are informative only, per IEEE 802.3ae.
2. Valid between 1260 and 1355 nm. Measured with worst ER; BER<10⁻¹²; 231– 1 PRBS.
3. Valid between 1260 and 1355 nm. Per IEEE 802.3ae.

V. Digital Diagnostic Specifications

10GBASE-LR SFP+ transceivers can be used in host systems that require either internally or externally calibrated digital diagnostics.

Parameter	Symbol	Min	Typ.	Max	Units	Ref.
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Accuracy

Internally measured transceiver temperature	$\Delta DD_{Temperature}$			3	°C	
Internally measured transceiver supply voltage	$\Delta DD_{Voltage}$			3	%	
Measured TX bias current	ΔDD_{Bias}			10	%	1
Measured TX output power	$\Delta DD_{Tx-Power}$			2	dB	
Measured RX received average optical power	$\Delta DD_{Rx-Power}$			2	dB	

Dynamic Range for Rated Accuracy

Internally measured transceiver temperature	DD _{Temperature}	-40	85	°C
Internally measured transceiver temperature	DD _{Voltage}	3.1	3.5	V
Measured TX bias current	DD _{Bias}	10	90	mA
Measured TX output power	DD _{Tx-Power}	-8.2	+0.5	dBm
Measured RX received average optical power	DD _{Rx-Powe}	-14.2	+0.5	dBm

Max Reporting Range

Internally measured transceiver temperature	DD _{Temperature}	-40	125	°C
Internally measured transceiver supply voltage	DD _{Voltage}	2.8	4.0	V
Measured TX bias current	DD _{Bias}	0	20	mA
Measured TX output power	DD _{Tx-Power}	-10	+2	dBm
Measured RX received average optical power	DD _{Rx-Power}	-22	+2	dBm

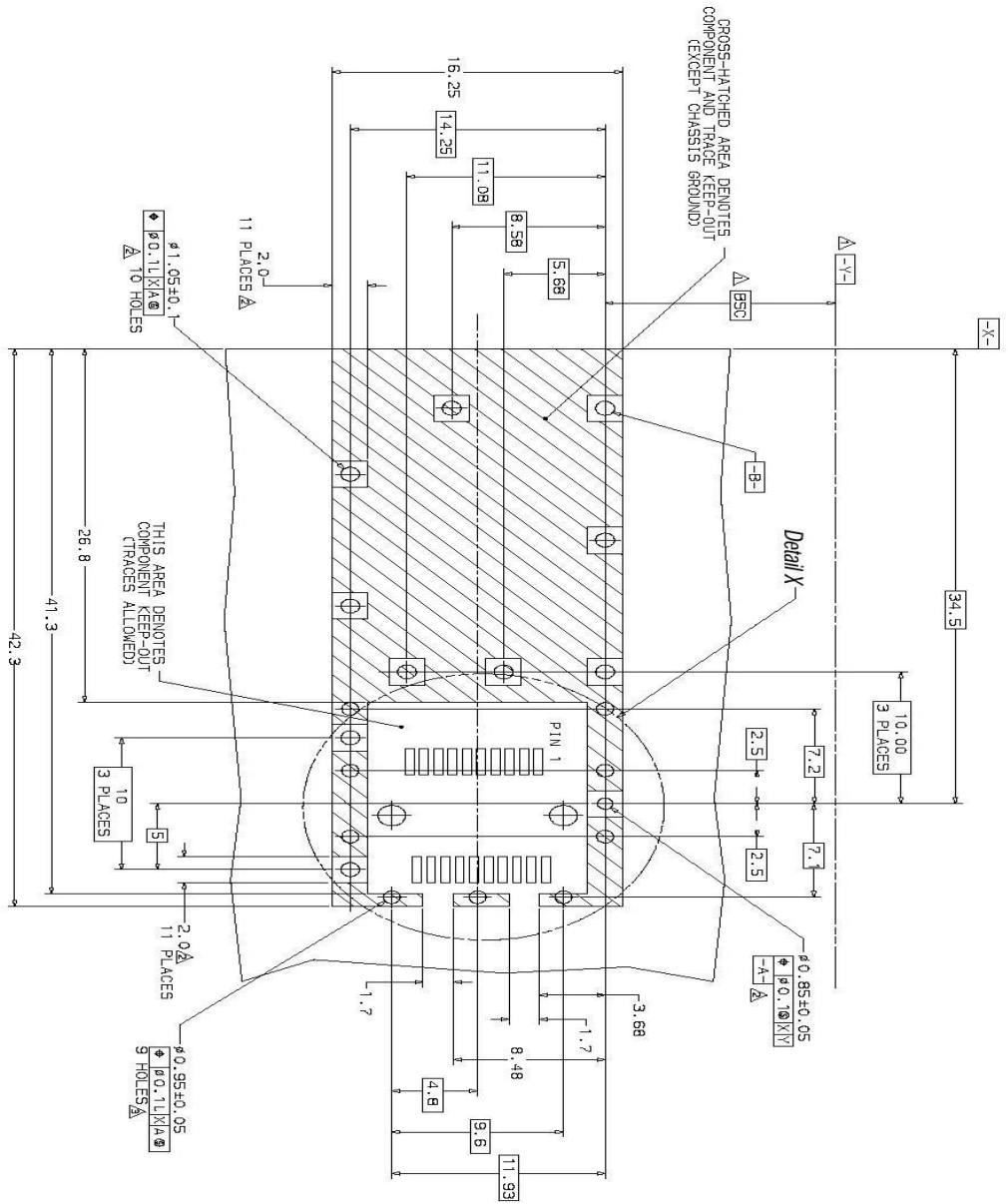
Notes:

Accuracy of measured Tx bias current is 10% of the actual bias current from the laser driver to the laser.

VI. Pin Description

Pin	Symbol	Name/Description	Ref.
1	V _{EET}	Transmitter Ground	1
2	T _{FAULT}	Transmitter Fault	2
3	T _{DIS}	Transmitter Disable. Laser output disabled on high or open.	3
4	SDA	2-wire Serial Interface Data Line	2
5	SCL	2-wire Serial Interface Clock Line	2
6	MOD_ABS	Module Absent. Grounded within the module	2
7	RS0	Rate Select 0.	4
8	RX_LOS	Loss of Signal indication. Logic 0 indicates normal operation.	5
9	RS1	Rate Select 1.	4
10	V _{EER}	Receiver Ground	1
11	V _{EER}	Receiver Ground	1
12	RD-	Receiver Inverted DATA out. AC Coupled.	
13	RD+	Receiver Non-inverted DATA out. AC Coupled.	
14	V _{EER}	Receiver Ground	1
15	V _{CCR}	Receiver Power Supply	
16	V _{CCT}	Transmitter Power Supply	
17	V _{EET}	Transmitter Ground	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V _{EET}	Transmitter Ground	1

VIII. Host Board SFP+ Connector Recommendations



△ Datum and Basic Dimension Established by Customer

▢ Pads and Vias are Chassis Ground, 11 Places

△ Through Holes are Unplated

